



Attorney Docket No. 0756-1630

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 08/799,506

Filed: February 12, 1997

For: METHOD FOR PROCESSING

SEMICONDUCTOR DEVICE

APPARATUS FOR PROCESSING A

SEMICONDUCTOR AND

APPARATUS FOR PROCESSING

SEMICONDUCTOR DEVICE

) Group Art Unit: 2822

) Examiner: M. Wilczewski

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is
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SUBMISSION OF FORMAL DRAWINGS

Honorable Commissioner of Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowability dated January 13, 2005, submitted herewith are 10 sheets of formal drawings consisting of Figures 1-11.

Respectfully submitted,

Eric J. Robinson
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